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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *MT*

This application is a DIV of 10/226,472 08/23/2002 PAT 6,891,248

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *MS*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/11/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 7	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>MT</i> Initials				

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## TITLE

Method for fabricating semiconductor component with on board capacitor

FILING FEE  RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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